COMPUTING

| 1. | Small Transistors are used in generation of computers. (March 06, June 09) |
|---|---|
| | All parts of computer are controlled by (2006, 2007, 2009) |
| 3. | Input, Output, CPU are of the computer. (June 2006) |
| 4. | An example for output is (June 2006) |
| 5. | Vacuum tubes are used in generation of computers (March 2007) |
| 6. | The language known to the computers is called (June 2009) |
| 7. | is used to make a diagrammatic representation of an algorithm (March 2008) |
| 8. | The father of computer is (March 2008) |
| | To express the algorithm in a language understandable by a computer is called |
| | The number of major parts in a computer is (June 2009) |
| | C.P.U means |
| | large amount of information is stored in unit of computers. |
| | The method of solving a problem is called |
| | are used in fourth generation of computers. |
| | All the mathematical operations are carried out in units. |
| | The input unit, C.P.U and output unit all together is called |
| | The unit that gains results from C.P.U is |
| | Example for computer language is |
| | The present day computers are made as generation computers. |
| | In the preparation of flow charts, we use Rhombus shaped box for |
| | A computer is an device. |
| | Printer is exemple for puit |
| | Printer is example for unit COBOL means |
| | The computers built in between 1950-1960 are called as generation of computers. |
| | is example for Input unit |
| | An algorithm means |
| | The Rhombus shaped box is used in a flow chart for |
| | Each computer consists of three essential units, namely Input unit, output unit and the unit. |
| | BASIC is language. |
| | Father of modern computers is |
| | are used in third generation of computers. |
| | |
| 33. | A.L.U means |
| 33. | |
| | KEY |
| 1. 5 | KEY Second |
| 1. 3 | KEY Second C.P.U |
| 1. S 2. G 3. I | KEY Second C.P.U Hardware |
| 1. S 2. G 3. I 4. J | KEY Second C.P.U Hardware printer |
| 1. S 2. G 3. I 4. I 5. I | KEY Second C.P.U Hardware printer First |
| 1. 3 2. 0 3. 1 4. 1 5. 1 6. 1 | KEY Second C.P.U Hardware printer First Higher language (or) software programming language |
| 1. S 2. G 3. I 4. I 5. I 6. I 7. I | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart |
| 1. \$ 2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit |
| 1. \$2. 0 3. 1 4. 1 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. 11. | KEY Second C.P.U Hardware orinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. 11. 12. | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. 11. 12. 13. | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. 11. 12. 13. 14. 15. | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. 11. 12. 13. 14. 15. | KEY Second C.P.U Hardware orinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 1 1 2. 1 3. 1 4. 1 5. 1 6. 1 1 7. 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware |
| 1. \$2. (3. 14. 15. 14. 15. 16. 17. 18. (4. 17. 18. 18. 14. 15. 16. 17. 18. 18. 18. 18. 18. 18. 18. 18. 18. 18 | KEY Second C.P.U Hardware printer First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. | KEY Second C.P.U Hardware brinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 11. 12. 13. 14. 15. 16. 17. 18. 19. 19. | KEY Second C.P.U Hardware brinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL IV th generation |
| 1. \$2. (3. 1) 4. 11 5. 1] 6. 13 7. 1 18. (9. 1) 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. | KEY Second C.P.U Hardware crinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL IV th generation Decision box |
| 1. \$2. (3. 1) 4. 1) 5. 1) 6. 1) 7. 1) 10. 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. 22. 23. | Second C.P.U Hardware orinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL IV th generation Decision box Eelectronic Flowchart Output |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. 22. 23. 24. | Second C.P.U Hardware orinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL IV th generation Decision box Eelectronic Flowchart Output Common business oriented language |
| 1. \$2. 0 3. 1 4. 1 5. 1 6. 1 7. 1 8. 0 9. 1 10. 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. 22. 23. 24. 25. | Second C.P.U Hardware orinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL IV th generation Decision box Eelectronic Flowchart Output Common business oriented language I st generation |
| 1. \$2. (3. 1) 4. 1) 5. 1) 6. 1) 7. 11 8. (9. 1) 11. 12. 13. 14. 15. 16. 17. 18. 19. 20. 21. 22. 23. 24. 25. 26. | Second C.P.U Hardware orinter First Higher language (or) software programming language Flow chart Charles Babbage Programming language 3 Central Processing Unit Memory Programme Very large scale integrated circuites Arithmetic and logical unit Hardware Out put COBOL (or) PASCAL IV th generation Decision box Eelectronic Flowchart Output Common business oriented language |

28. Decision making29. Central Processing Unit (C.P.U.)

- 30. Computer
- 31. Von Newmann
- 32. Very small electronic circuits
- 33. Arithmetic and Logic unit

Important symbols

| 1. Negation | ~ |
|------------------------------|--------------------------------|
| 2. And | ٨ |
| 3. Or | V |
| 4. Implie | \Rightarrow |
| 5. If and only if | \Leftrightarrow |
| 6. For all | A |
| 7. For some | Ξ |
| 8. Belongs | € |
| 9. Not belongs | ∉ |
| 10. Subset | _ |
| 11. Superset | n U |
| 12. Union | U |
| 13. Intersection | \cap |
| 14. Powerset | μ |
| 15. Null set | ф |
| 16. Complement of A | A^1/A^c |
| 17. Cartesian product of | |
| A, B is | $\mathbf{A} \times \mathbf{B}$ |
| 18. Identity function | I (A) |
| 19. Discriminant | Δ or D |
| 20. Transpose of A | \mathbf{A}^{T} |
| 21. Inverse of A | A^{-1} |
| 22. Fistle function A to B | f:A→B |
| 23. Composite function of f | Î |
| and g | gof |
| 24. Sum of first 'n' natural | Σn |
| numbers | |
| 25. n th term | t _n |
| 26. Sum of 'n' terms | s _n |
| 27. Arithmetic mean | X |
| 28. Sum of frequencies | Σf or N |
| | |