

UNIVERSITY OF CALICUT
(Pareeksha Bhavan)

EG I/2/7138/2010

Calicut University.PO
673 635
Dated: 28-12-2011

NOTIFICATION

In continuation of the notification of even no. dated 16-12-2011 it is notified for the information of all concerned that the First semester M.Tech Examinations in the following branches will be conducted at **Nehru College of Engineering & Research Centre, Pambady** as per the Time Table given below.

1. Applied Electronics & Communication Systems
2. Power Electronics & Drives
3. Machine Design

REVISED TIME TABLE FOR THE FIRST SEMESTER M.TECH.
EXAMINATIONS-DECEMBER 2011

Centre of Examination : Nehru College of Engineering & Research
Centre, Pambady
Time of Examination : 1.30 pm to 4.30 pm (Except on Friday)
2.00 pm to 5.00 pm (On Friday)
Maximum Marks : 100 each

Day & Date	Subject
APPLIED ELECTRONICS & COMMUNICATION SYSTEMS	
Wednesday 4 th January 2012	EAC 10 101 Mathematics for Communication Engineering
Friday 6 th January 2012	EAC 10 102 Design of Digital Signal Processing Systems
Monday 9 th January 2012	EAC 10 103 Advanced Digital Communication
Wednesday 11 th January 2012	EAC 10 104 System Design Using Embedded Processors
Friday 13 th January 2012	EAC 10 105A Information Theory
POWER ELECTRONICS & DRIVES :	
Wednesday 4 th January 2012	EPD 10 101 Applied Mathematics
Friday 6 th January 2012	EPD 10 102 System Dynamics
Monday 9 th January 2012	EPD 10 103 Analysis of Power Electronic Circuits-I
Wednesday 11 th January 2012	EPD 10 104 Electric Drives
Friday 13 th January 2012	EPD 10 105D High Voltage DC and AC Transmission
MACHINE DESIGN	
Wednesday 4 th January 2012	MMD 10 101 Advanced Engineering Mathematics
Friday 6 th January 2012	MMD 10 102 Advanced Mechanics of Solids
Monday 9 th January 2012	MMD 10 103 Theory of Vibrations
Wednesday 11 th January 2012	MMD 10 104 Design Engineering
Friday 13 th January 2012	MMD 10 105B Mechatronics System Design

Sd/-
CONTROLLER OF EXAMINATIONS