

UNIVERSITY OF CALICUT
(Pareeksha Bhavan)

REVISED TIME TABLE FOR THE SECOND SEMESTER M.TECH.
INTERNAL EXAMINATION-JUNE 2012

Centre : NIELIT, Calicut
Time of Examination : 9.30 am to 12.30 pm

Day and Date	Subject
EMBEDDED SYSTEMS	
Monday 4 th June 2012	ES 10 201 Design of Digital Signal Processing Systems
Wednesday 6 th June 2012	ES 10 202 Embedded OS & RTOS
Friday 8 th June 2012	ES 10 203 Product Design and Quality Management
Monday 11 th June 2012	ES 10 204 A (Elective-II) Embedded Applications in Power Conversion
Wednesday 13 th June 2012	ES 10 205 C (Elective-III) Multimedia Compression Techniques
ELECTRONICS DESIGN TECHNOLOGY	
Monday 4 th June 2012	EDT 11 201 Design of Digital Signal Processing Systems
Wednesday 6 th June 2012	EDT 11 205C (Elective-III) Embedded OS & RTOS
Friday 8 th June 2012	EDT 11 203 Product Design & Development
Monday 11 th June 2012	EDT 11 204A (Elective-II) Embedded Applications in Power Conversion
Wednesday 13 th June 2012	EDT 11 202 Multimedia Compression Techniques

Calicut University.PO
Dated: 30-05-2012

Sd/-
CONTROLLER OF EXAMINATIONS