NILAMBER-PITAMBER UNIVERSITY MEDININAGAR PALAMU – 822101 of B.Tech 2012 (IVth Semester) Examination (Timing – 12:30 PM

Programme of B.Tech 2012 (IV th Semester) Examination (Timing - 12:30 PM – 03:30 PM)				
Date & Days	Subjects	Branch		
24/04/2012 (Tuesday)	Advance Strength of Materials	MECHANICAL ENGG.		
	Principles Communication Engg.	INFORMATION TECH.		
	Data Communication	APPLIED ELECT. & INSTRUMENTATION		
	Data Communication	COMPUTER SCIENCE & ENGG.		
	Data Communication	ELECTRONICS & COMM. ENGG.		
	Power System-I	ELECTRICAL & ELECT. ENGG.		
25/04/2012 (Wednesday)	Electrical Machines	MECHANICAL ENGG.		
	Discrete Mathematics	INFORMATION TECH.		
	Electromagnetics	APPLIED ELECT. & INSTRUMENTATION		
	Discrete Mathematics	COMPUTER SCIENCE & ENGG.		
	Electromagnetics	ELECTRONICS & COMM. ENGG.		
	Electromagnetic Theory	ELECTRICAL & ELECT. ENGG.		
	Manufacture Science-I	MECHANICAL ENGG.		
	Digital Circuit Design	INFORMATION TECH.		
26/04/2012 (Thursday)	Digital Electronics Circuits	APPLIED ELECT. & INSTRUMENTATION		
	Digital Circuit Design	COMPUTER SCIENCE & ENGG.		
	Electronic Circuits	ELECTRONICS & COMM. ENGG.		
	Digital Electronics	ELECTRICAL & ELECT. ENGG.		
	Kinematics of Machine	MECHANICAL ENGG.		
	Data Structure & Programming Methodology	INFORMATION TECH.		
27/04/2012	Linear IC Applications	APPLIED ELECT. & INSTRUMENTATION		
(Friday)	Data Structure & Programming	COMPUTER SCIENCE & ENGG.		
	Linear ICs & Applications	ELECTRONICS & COMM. ENGG.		
	Electrical Machines-I	ELECTRICAL & ELECT. ENGG.		
	Mechanical Measurement & Metrology	MECHANICAL ENGG.		
	Control System	INFORMATION TECH.		
28/04/2012	Network Theory	APPLIED ELECT. & INSTRUMENTATION		
(Saturday)	System Analysis & Design	COMPUTER SCIENCE & ENGG.		
	Network Theory	ELECTRONICS & COMM. ENGG.		
	Circuit Theory	ELECTRICAL & ELECT. ENGG.		
	Industrial Engineering	MECHANICAL ENGG.		
	Analysis & Design of Information System	INFORMATION TECH.		
30/04/2012	Solid State Devices	APPLIED ELECT. & INSTRUMENTATION		
(Monday)	System Software	COMPUTER SCIENCE & ENGG.		
	Solid State Devices	ELECTRONICS & COMM. ENGG.		
	Solid State Devices	ELECTRICAL & ELECT. ENGG.		

Centre of Examination

S.No.	Centre	College/Institute	Centre Superintendent
1.	G.L.A. College, Medininagar	D.A.V. Institute of Engineering & Technology, Medininagar	Prof-in-charge, G.L.A. College, Medininagar
2.	S.S.J.S.N. College, Garhwa	R.C.I.T., Bishrampur	Prof-in-charge, S.S.J.S.N. College, Garhwa

Note :-

- 1. In case of stay-out/walk-out/mass scale use of unfair means, there will be no re-examination and Zero marks will be awarded.
- 2. In case of misprint regarding examination programme in the newspaper, the University is not likely to be responsible and no re-examination will be held. The examinees concerned are therefore directed to verify the examination programme from their respective college.
- 3. In case of gross indiscipline by the examinees (assault to invigilators or any person, tearing of Answer books, forcible prevention of other examinees from appearing at the examination and damage to property etc.) they will make themselves liable to be debarred from examination may also lead to their rustication.

By order of the Vice-Chancellor

Sd/-(Lal G.S.N. Shahdeo) Controller of Examinations N.P. University, Medininagar

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- 1. All the Principals of the Institute concerned
- 2. The concerned Centre Superintendents
- 3. The Deputy Commissioner, Palamu / Garhwa
- 4. Superintendent of Police, Palamu / Garhwa
- 5. S.D.O., Palamu / Garhwa
- 6. Secretary to VC/PVC and Registrar for information to VC/PVC/R
- 7. The Finance Officer, N.P. University, Medininagar, Palamu
- 8. The Editor -

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Controller of Examinations Nilamber-Pitamber University Medininagar, Palamu