

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA**  
**EXAMINATION BRANCH**  
M.TECH II SEMESTER (07,08 ADMITTED BATCHES ONLY)- SUPPLEMENTARY EXAMINATIONS- October,2012  
**T I M E T A B L E**

TIME: 10.00 AM To 1.00. PM

Date & Day	MACHINE DESIGN	CAD/CAM	----	----
15-10-2012 Monday	Design Synthesis	Industrial Robotics	----	---
17-10-2012 Wednesday	Experimental Stress Analysis	Design of Hydraulics and Pneumatics Systems	----	---
19-10-2012 Friday	Optimization Techniques	Intelligent Manufacturing Systems	----	---
23-10-2012 Tuesday	Computational Fluid Dynamics	2.Computer Aided Process Planning	----	
25-10-2012 Thursday	----	Automation in Manufacturing	----	---
29-10-2012 Monday	----	Computer Aided Manufacturing	----	---
31-10-2012 Wednesday	----	Flexible Manufacturing Systems	----	---

- NOTE: i IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS WILL BE CONDUCTED AS USUAL  
ii ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.  
iii THE PRINCIPALS ARE REQUESTED TO INFORM THE UNIVERSITY ANY OTHER SUBSTITUTE SUBJECTS THAT ARE NOT INCLUDED IN THE ABOVE LIST IMMEDIATELY

*A.m.prasad*

Date: 26-09-2012

Controller of Examinations

**M. Tech Time Table for EEE**

<b>Date &amp; Day</b>	<b>Power &amp; Industrial Drives</b>	<b>Power Electronics</b>	<b>PSC &amp; A</b>	<b>Power Systems (High Voltage)</b>	<b>Power Electronics &amp; Electric Drives</b>
15-10-2012 Monday	Power Electronics Control of AC Drives	----	Power Systems Control & Stability	High Voltage Testing Techniques	Power Electronics Control of AC Drives
17-10-2012 Wednesday	Flexible AC Transmission Systems	-----	Flexible AC Transmission Systems	Flexible AC Transmission Systems	-----
19-10-2012 Friday	Dynamics of Electrical Machines	----	Power System Deregulation	Partial Discharge in HV Equipment	Dynamics of Electrical Machines
23-10-2012 Tuesday	High Voltage DC Transmission	Neural & Fuzzy Systems	Neural & Fuzzy Systems	Surge Phenomena and Insulation Coordination	High Voltage DC Transmission
25-10-2012 Thursday	2. Advanced Digital Signal Processing	----	Real Time Control of Power Systems	Power System Protection with Static Relays	2. Advanced Digital Signal Processing
29-10-2012 Monday	1. Reliability Engineering	-----	Power Quality	Power System Operation Control	Reliability Engineering
31-10-2012 Wednesday					

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### M. Tech Time Table for CSE

<b>Date &amp; Day</b>	<b>Software Engineering</b>	<b>Computer Science and Engineering</b>	<b>Information Technology</b>	<b>----</b>
15-10-2012 Monday	Information Security	Object Oriented Analysis and Design	-----	-----
17-10-2012 Wednesday	Data Ware Housing and Mining	Data Ware Housing and Mining	Network Management Systems	----
19-10-2012 Friday	Software Quality Assurance and Testing	Network Security and Cryptography	Design Patterns	-----
23-10-2012 Tuesday	Software Architecture and Design Pattern	Embedded Systems	---	----
25-10-2012 Thursday	1. Image Processing and Pattern Recognition	1. Neural Networks 2. Advanced Computer Architecture 3. Pattern Recognition and Image Processing	---	-----
29-10-2012 Monday	Information Retrieval Systems	2. Software Project Management .	Information Retrieval Systems	----
31-10-2012 Wednesday	Middle Ware Technologies	---	-----	-----

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**M. Tech Time Table for ECE**

<b>Date &amp; Day</b>	<b>VLSI SYSTEM DESIGN</b>	<b>VLSI &amp; EMBEDDED SYSTEMS</b>	<b>EMBEDDED SYSTEMS</b>	<b>DIGITAL ELECTERONICS &amp; COMMUNICATIO N SYSTEMS</b>	<b>DIGITAL SYSTEMS &amp; COMPUTER ELECTRONICS</b>	<b>ELECTRICAL COMMUNICATI ONAL ENGINEERING</b>	<b>SYSTEMS AND SIGNAL PROCESSING</b>
15-10-2012 Monday	Algorithms for VLSI Design Automation	Algorithms for VLSI Design Automation	Embedded Software Design	Speech Processing	Speech Processing	Telecommunicati on Switching	Speech Processing
17-10-2012 Wednesday	Design for Testability	Real Time Operating Systems For Embedded Systems	1.Real Time Operating Systems For Embedded Systems 2. Design for Testability	Image Processing	Image Processing	---	Image Processing
19-10-2012 Friday	1.Network Security and Cryptography 2. Hardware Software Co-Design 3. System Modeling & Simulation	Advanced Digital Filtering	1.Wireless Communication & Networks 2.Advanced Digital Filtering	Wireless Communication & Networks	Wireless Communication & Networks	Cryptography and Network Security	--
23-10-2012 Tuesday	Low Power VLSI Design	VLSI Technology and Design	Low Power VLSI Design	1.Low Power VLSI Design	1.Low Power VLSI Design	---	--
25-10-2012 Thursday	Scripting Language for VLSI Design Automation	CPLD and FPGA architecture and Applications	CPLD and FPGA architecture and Applications	2. High Speed Networks	--	Embedded & Real Time Systems	
29-10-2012 Monday							
31-10-2012 Wednesday							

02-11-2012 Friday	1.DSP Processors & Architectures	1.DSP Processors & Architectures	1.DSP Processors & Architectures	1.DSP Processors & Architectures	--	--	--
05-11-2012 Monday	---	Image and Video Processing	---	Coding Theory & Techniques	--	--	--
07-11-2012 Wednesday	--	--	--	Micro Computer System Design	--	--	--

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