

**VFSTR UNIVERSITY:: VADLAMUDI**

**Time Table for M.Tech. I Year I Semester Supplementary (R14) Examinations January – 2016**  
(2014 admitted batch)

Release Date: 29.12.2015

**Examination Time: 01:00 PM to 04:00 PM**

Date, & Specialization	CSE	Machine Design
20.01.2016 (Wednesday)	Mobile Computing (CS615)	Advanced Numerical Methods In Engineering (ME527)

**Time Table for M.Tech. I Year I Semester (R10) Supplementary Examinations January - 2016**  
(2012 admitted batch)

**Examination Time: 01:00 PM to 04:00 PM**

Date, & Specialization	Embedded Systems
20.01.2016 (Wednesday)	Digital & Analog IC Design (EC503)
21.01.2016 (Thursday)	Embedded Programming Languages (EC513)
22.01.2016 (Friday)	Basic VLSI Design (EC507)

  
**Controller of Examinations**

  
**Dean – AR & Evaluation**

Copy to PA to VC, PA to Registrar, PA to Dean – E&M, All HODs, Web Administrator, Training Cell, Placement Cell, Bus I/C, Reception, Workshop I/C.

VU/CE/CIR/15/1058 (TS) Date: 29.12.2015