

## VFSTR UNIVERSITY:: VADLAMUDI

### Time Table for M.Tech. I Year II Semester (R10, R13) Supplementary Examinations October – 2014 (2011, 2012, 2013 Admitted batches)

Time: 10:00 AM to 01:00 PM

Date & Branch	Computer Science and Engineering	Embedded Systems	Machine Design	Power Systems
07.10.2014 (Tuesday)	Data warehousing and Mining (CS502) (R10)	RTOS for Embedded Systems (EC502) (R10)	Mechanical Vibrations (ME504) (R10)	Power Quality (EE512) (R10)
09.10.2014 (Thursday)	Network Security and Cryptography (CS504) (R10, R13)	FPGA and CPLD Architectures and Applications (EC526) (R10)	---	Flexible AC Transmission Systems (EE508) (R10)
11.10.2014 (Saturday)	Embedded Systems (CS508) (R13)	DSP Processors and Architectures (EC508) (R10)	---	Power System Dynamics & Stability (EE520) (R13)
13.10.2014 (Monday)	Digital Image Processing (CS510) (R10)	Micro Electro Mechanical Systems (EC536) (R10)	---	---

**Controller of Examinations**

**Dean - Evaluation**

Copy to PA to VC, PA to Registrar, PA to Dean-E&M, All HODs, Exam cell I/C, TC, PC, Workshop I/C, Bus I/C, Reception, Web Administrator  
VU/CE/CIR/14/805 (TS) Date: 10.09.2014