DEPARTMENT OF INFORMATION TECHNOLOGY
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL-25

MAKE-UP EXAMINATION TIME TABLE FOR
JULY 2015 -DECEMBER 2015

III SEMESTER (B.Tech.)   Hall: L601

DATE & DAY	TIME	SUBJECT
15.12.2015 TUESDAY	9:00 AM TO 12:00PM	IT200-Data Structures & Algorithms

V SEMESTER (B.Tech.)   Hall: L601
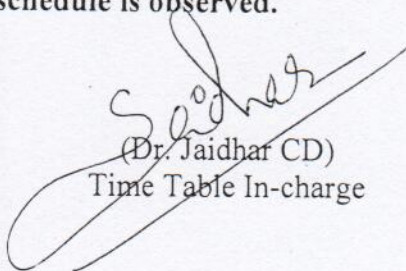
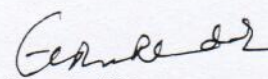|		
15.12.2015 TUESDAY	02:00 PM TO 5:00 PM	IT302-Web Technologies and Applications
18.12.2015 FRIDAY	09:00 AM TO 12:00 PM	IT412-Semantic Web Technologies
18.12.2015 FRIDAY	2:00PM TO 5:00PM	IT300-Parallel Computing

VII SEMESTER (B.Tech.)   Hall: L601

16.12.2015 WEDNESDAY	9:00AM TO 12:00PM	IT411-Mobile Adhoc Networs
16.12.2015 WEDNESDAY	2:00PM TO 5:00PM	IT400 - Mobile Computing
17.12.2015 THURSDAY	9:00AM TO 12:00PM	IT355 - Soft Computing
17.12.2015 THURSDAY	2:00PM TO 5:00PM	IT414 - Rich Internet Applications

Note: Please inform time table in-charge on or before 11/12/15, 5:00PM, if the clash in examination schedule is observed.

(Dr. Jaidhar CD)
Time Table In-charge

Head of Department

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG., NITK SURATHKAL

Make-up Examinations – ODD Semester 2015-16
Time table

	15-12-15 Tues		16-12-15 Wed		17-12-15 Thurs		18-12-15 Fri		19-12-15 Sat	
	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN
I Sem B.Tech.									****EC110 EECE**	
III Sem B.Tech.	EC222 EMW		EC232 DSA		EC223 AE		EC220 DECA			
V Sem B.Tech.	EC280 DSD		EC321 DC		EC320 AIC					
VII Sem B.Tech.							EC445 NT / EC447 PRML			
I Sem M.Tech. (CE)	CE822 WCN									
I Sem M.Tech. (VL)	VL721 AICD									

FN: 9.00 AM to 12.00 Noon   AN: 2.00 PM to 5.00 PM   Venue: EC Seminar hall

** NTB 001 (for EC110)

B.Tech. Courses

EC110 Elements of Electronics & Communication Engg.
EC220 Digital Electronics & Computer Architecture
EC222 Electromagnetic Waves
EC223 Analog Electronics
EC280 Digital System Design
EC232 Data Structures & Algorithms
EC320 Analog Integrated Circuits
EC321 Digital Communication
EC445 Number theory and Applications in E&C Engg.
EC447 Pattern recognition and machine learning

M.Tech. Courses

CE822 Wireless Communication & Networks
VL721 Analog IC Design

Head of the Dept., E&C Engg.