

**KURUKSHETRA UNIVERSITY KURUKSHETRA**  
*(Established by the State Legislature Act XII of 1956)*  
*("A" Grade, NAAC Accredited)*

Date-Sheet for the M.Tech. Microelectronics & VLSI Design Ist Semester (New Scheme) examination  
 commencing w.e.f. 09-12-2014.

**(THEORY EXAMINATION)**

Time of Examination: 9.30 A.M. to 12.30 P.M. (MORNING SESSION)

Examination Centre : A-867, Kurukshetra-44, Department of Electronics

DATE	SUBJECT & PAPER
	<u>I Semester</u>
9-Dec-2014	Paper-MMVD 101: Process Technology for ULSI-I
13-Dec-2014	Paper-MMVD 102: MOSFET Physics and Sub-Micron Device Modeling
16-Dec-2014	Paper-MMVD 103: VLSI Design
19-Dec-2014	Paper-MMVD 104: Digital Signal Processing

- NOTES :**
- 1 On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.
  - 2 Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.
  - 3 No extra answer-sheet will be provided.

KURUKSHETRA  
4-Dec-2014

CONTROLLER OF EXAMINATIONS