



K L University

Examination Branch

I/II M.Tech I Semester Regular Examinations - January 2015 (Y14 Batch)

TIME TABLE

Timings: 9.30 AM to 12.30 PM

M.Tech	02.1.2015	04.1.2015	06.1.2015	08.1.2015	10.1.2015	12.1.2015
Biotechnology	Mathematics & Biostatistics 12BT501	Biochemical Reaction Engg. 12BT502	Molecular Biology & rDNA Technology 12BT504	Applied Bioinformatics 12BT503	Medical Biotechnology 12BTE531	Protein Engineering 12BTE530
Structural Engineering	Applied Mathematics 11CE501	Theory of Elasticity 11CE502	Structural Dynamics 11CE503	Advanced Prestressed Concrete 11CE504	Repaire & Rehabilitation of Structures 11CE531	Geotechnical Earthquack Engineering 11CE541
Construction Technology & Management	Construction Technology 14CT501	Construction Materials 14CT502	Construction Planning Scheduling and Control 14CT503	Statistical Methods for Management 14CT504	High Performance Building 14CT531	Construction Personal Management 14CT541
Computer Science & Engineering	Mathematical Foundations of Computer Science 14CS501	Computer Organization and Architecture 14CS502	Data Structures and Algorithms 14CS503	D D B M S 14CS504	Elective 1: Soft Computing 14CS531	Elective 2: Requirement Engineering 14CS535
Computer Networks	Data Networks 14CN501	Network Programming 14CN502	Applied Cryptography 14CS503	Secure Coding 14CN504	Elective 1: Network Routing 14CN531	Elective 2: Adhoc Networks 14CN536
Cloud Computing	Enterprise Devices and Networks 14CC501	Enterprise Servers and Storage 14CC502	IT Service Management 14CC503	Web Application Development 14CC504	Elective 1: Big Data Analysis 14CC531	Elective 2: Application Development Frame Works 14CC536
Communication & Radar Systems	Modern Digital Communication 13EC501	Radiating Systems 13EC502	Microwave & Millimeter Wave Circuits 13EC503	Image and Video Processing 13EC520	Coding Theory 13EC533	Optical Networks 13EC582
VLSI	Algorithm for VLSI Design Automation 13EC551	HDL & PLD Architectures 13EC552	IC Fabrication Technology 13EC553	Nano Electronics 13EC591	Semiconductor Device Modeling 13EC592	Mos Circuit Design 13EC550
Signal Processing	Bio Medical Signal Processing 13EC526	Mathematics for Signal Processing 13EC532	Multirate Signal Processing 13EC529	Image and Video Processing 13EC520	Speech Processing 13EC524	Wavelet Theory and Applications 13EC531
Power Systems	Power System Dynamics and Stability 14EE511	Advanced Power System Analysis 14EE512	Optimization Techniques 14EE503	Modern Control theory 14EE504	Reactive Power Compensation and Management 14EE541 Distribution System Planning & Automation 14EE542	Non Conventional Energy Resouces 14EE533 Power System Restructuring Deregulation and Power Markets 14EE544
Power Electronics & Drives	Design of Power Converters 14EE501	Power Electronic Control of Drives 14EE502	Optimization Techniques 14EE503	Modern Control theory 14EE504	Special Machines 14EE532	Digital Signal Processor 14EE534 Non Conventional Energy Resources 14EE533
Embedded Systems	Micro Controllers for Embedded Systems 11EM501	Real Time Concepts for Embedded Systems 12EM502	VLSI Technology & Design 13EM503	Wireless Coommunication and Networks 12EM504	CPLD & FPGA Architecture and Applications 13EME30	Real Time Operating Systems 12EME44
Wireless Communication & Sensor Networks	Computational Methods & Error Analysis 13EM511	Sensor & Sensing Principles 13EM513	Data Acquisition & Hardware Networks 13EM514	Wireless Communication and Networks 13EM512	Advanced Wireless Networks 13EM531	RF System Design for Wireless Communicaton 13EM533 Data Management Systems 13EM535
Thermal Engineering	Neumerical Methods in Thermal Engineering 13TE501	Advanced Thermodynamics 13TE502	Design of Thermal Systems 13TE503	Advanced Heat & Mass Transfer 13TE504	Heat Exchanger Design 13TE531	IC Engine Combusion and Pollution 13TE542
Mechatronics	Fundamentals of Mechatronics 11MT501	Advanced Engineering Mathematics 13MT502	Sensor & Actuators 13MT503	Modeling & Simulation of Mechatronics Systems 13MT504	MEMS & MEMS 13MT534	Industrial Automation 13MT631

Note: Practical Examinations for Regular candidates will commence from 30th and 31st December 2014. Detailed Time Table will be given by the Departments.

Controller of Examinations

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