



GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi - 110078

EXAMINATION DIVISION

Conduct Branch-I

FINAL THEORY DATE SHEET FOR END TERM EXAMINATIONS (DEC 2015 – JAN 2016)

M. Tech. (IT/CSE/IS/ECE/DC/SP/RA/RF & Microwave/VLSI Design/DWC) – Regular Programme

Date/Day	Morning Session (10:00 AM to 1:00 PM)	Evening Session (2:00 PM to 5:00 PM)
14.12.2015 Monday	MECS-603 Advanced Software Engineering MEDC-601 Advanced Digital Communication MERA-601 Computational Mathematics MERF-601 Advanced Electromagnetic Engineering MEIT -601 Introduction to Computer Security MEIT -603 Introduction to Computer Security MEVS-601 Digital System Design using Verilog	
16.12.2015 Wednesday	MECS-615 Theory of Computation MEVS-611 Wireless Networks MEEC-611 Telecommunications System Modeling & Simulation MERA-609 Control System MEDC-605 Advanced Signal Processing MESP-603 Analog Signal Processing MEIS-609 Decision Support Systems & Methods	
18.12.2015 Friday	MECS-605 Advances in Data & Computer Communication MEEC-601 Optoelectronics and Optical Fibre Communication MERA-603 Robotics Engineering MEDC-603 Optical Fiber Communication Systems MESP-601 Signal Theory (ST) MEIS-605 Cryptography Foundation and its Application	
21.12.2015 Monday	MECS-601 Advanced Data Structures MERA-619 Mechatronics Systems and Applications MERF-605 RF and Passive Circuits Design MEVS-609 Algorithm Analysis and Design	

Date/Day	Morning Session (10:00 AM to 1:00 PM)	Evening Session (2:00 PM to 5:00 PM)
23.12.2015 Wednesday	MEVS-603 VLSI Technology MERA-605 Introduction to Manufacturing Systems MERF-603 Microwave Theory and Circuits MECS-607/MEIS-607/MECS-613 Advanced Operating Systems	
29.12.2015 Tuesday	MECS-607 Advanced Computer Architecture MEDC-607 Computer Communication Networks (CCN)	MECS-711 Software Quality Management MEEC-709 Multimedia Communication MESP-707 Speech Signal Processing MERF-705 RF IC MEIS-705 Biometric Security MEVS-705 Analog-Mixed Signal Design
30.12.2015 Wednesday	MECS-609 Enterprise Computing using JAVA MEEC-607 Advanced Computer Networks MEIS-601 Advanced Computer Network MERA-607 Introduction to Electrical and Electronics Systems MERF-607 Radar System (RS) MEVS-605 Advanced VLSI Design	
31.12.2016 Thursday	MECS-611 Computational Techniques using MATLAB	MECS-703 Advanced Software Testing MEEC-715 Broadband Access Technology MEDC-707 Spread Spectrum Technique MESP-709 VLSI Design of DSP Circuits MERF-711 RF MEMS MEVS-703 VLSI Design Test and Testability
04.01.2016 Monday		MECS-705 Cloud Computing MEEC-701 Adhoc Sensor Networks MESP-701 Advanced Digital Signal Processing MERF-701 Electromagnetic Interference and Compatibility in System Design MEIS-701 Mobile & Wireless Network Security
06.01.2016 Wednesday		MEIT-703 Information Theory & Coding MEEC-703 Digital Image Processing MEEC-721 Microwave Planar Transmission Lines & Circuits MESP-715 Selected Topics in Analog IC Design MERF-717 Wireless Adhoc Networks MERF-707 Smart Antennas for Mobile Communication MEIS-719 Security issues in Information Systems MEVS-713 MEMS and IC Integration MEDC-701 Advanced Mobile Computing

Date/Day	Morning Session (10:00 AM to 1:00 PM)	Evening Session (2:00 PM to 5:00 PM)
08.01.2016 Friday		MECS-701 Advanced Data Warehousing & Data Mining MECS-701 Data Ware House and Data Mining MEDC-703 Broadband Communication Systems and Networks MESP-703 Statistical Signal Processing MERF-703 Microwave and Millimeter Integrated Circuits MEVS-701 Algorithm for VLSI Physical Design Automation
11.01.2016 Monday		MECS-717 Cyber Crime Investigations and Crime Forensics MEIS-717 Cyber Crime Investigation & Cyber Forensics MEDC-709 Selected Topics on Recent Technologies in Wireless & Mobile Communication MECS-715 Advanced Multimedia MEIT-715 Advanced Multimedia MEVS-709 Designing with FPGA

Examination Centre: Banarsidas Chandiwala Institute of Information Technology, Chandiwala Estate, Maa Anandmai Ashram Marg, Kalkaji, New Delhi-110019, Ph: 26382282/83.

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For any clarification and query please contact:

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Dy. Registrar (Examinations)

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