VIGNAN UNIVERSITY:: VADLAMUDI

Time Table for Special Supplementary Examination – September 2013

(B.Tech. students who are having only one backlog to get degree are eligible)

Date of Examination: 23.09.2013

Examination Time: 10.00 AM to 1.00 PM

Subjects offered:

S.No	Subject Code	Subject Name
1	HS145	ENGINEERING MATHEMATICS-II
2	CS201	DATA STRUCTURES
3	EE107	BASIC ELECTRICAL & ELECTRONICS ENGINEERING
4	CS401	INFORMATION SECURITY
5	EC306	MICROPROCESSORS & INTERFACING
6	EC403	TRANSMISSION LINES & NETWORKS
7	EC204	ELECTRONIC CIRCUIT ANALYSIS
8	EC206	PULSE & DIGITAL CIRCUITS
9	EC310	COMPUTER ARCHITECTURE & ORGANIZATION
10	EC208	SWITCHING THEORY & LOGIC DESIGN
11	EC202	PROBABILITY THEORY & STOCHASTIC PROCESS
12	EE235	ELECTRICAL TECHNOLOGY
13	HS281	GENERAL STUDIES-I
14	ME401	OPERATIONS RESEARCH
15	HS141	ADVANCED COMMUNICATION SKILLS
16	EC417	DIGITAL DESIGN THROUGH VERILOG
17	MS407	PRODUCTION & OPERATIONS MANAGEMENT
18	MT405	MICROPROCESSORS & MICROCONTROLLERS
19	HS203	PROBABILITY & STATISTICS
20	MT210	PRODUCTION PROCESSES
21	MT101	INTRODUCTION TO MECHATRONICS

Controller of Examinations   **Director of Evaluation**