

UNIVERSITY OF CALICUT  
(Pareeksha Bhavan)

**TIME TABLE FOR THE SECOND SEMESTER M.TECH. INTERNAL  
REGULAR/SUPPLEMENTARY EXAMINATION-JUNE -2013**

Centre : National Institute of Electronics and IT , Calicut  
Time of Examination : 9.30 am to 12.30 pm

<b>Day and Date</b>	<b>Subject</b>
<b>EMBEDDED SYSTEMS</b>	
Friday 14 <sup>th</sup> June 2013	ES 10 201 Design of Digital Signal Processing Systems
Monday 17 <sup>th</sup> June 2013	ES 10 202 Embedded OS & RTOS
Wednesday 19 <sup>th</sup> June 2013	ES 10 203 Product Design and Quality Management
Friday 21 <sup>st</sup> June 2013	ES 10 204C Elective-II Information Security (2012 Admn)
Monday 24 <sup>th</sup> June 2013	ES 10 205C Elective-III Multimedia Compression Techniques
<b>ELECTRONICS DESIGN TECHNOLOGY</b>	
Friday 14 <sup>th</sup> June 2013	EDT 11 201 Design of Digital Signal Processing Systems
Monday 17 <sup>th</sup> June 2013	EDT 11 205 C Elective-III Embedded OS & RTOS
Wednesday 19 <sup>th</sup> June 2013	EDT 11 203 Product Design & Development
Friday 21 <sup>st</sup> June 2013	EDT 11 204 A Elective-II Information Security (2012 Admn)
	EDT 11 204 A Embedded Application in Power Conversion (2011 Admn)
Monday 24 <sup>th</sup> June 2013	EDT 11 202 Multimedia Compression Techniques

Calicut University.PO  
Dated: 04.06.2013

Sd/-  
CONTROLLER OF EXAMINATIONS