

UNIVERSITY OF CALICUT
(Pareeksha Bhavan)

TIME TABLE FOR THE B.Sc ELECTRONICS & COMPUTER SCIENCE
VI, IV & II SEMESTER(NON CCSS) SUPPLEMENTARY EXAMINATIONS
SEPTEMBER 2012

Time of Examination : 1.30 pm to 4.30 pm(Except Fridays)
: 2.00pm to 5.00 pm (On Fridays) Maximum Marks: 75 each

Day and Date	Subject
SIXTH SEMESTER	
Tuesday 4 th September 2012	E 601 T Digital Signal Processing C 601 T Computer Architecture
Thursday 6 th September 2012	E 602 T Radio & Television Engineering C 602 T Computer Graphics
Monday 10 th September 2012	E 603 T Modern Communication Systems C 603 T Computer Networks
FOURTH SEMESTER	
Wednesday 5 th September 2012	E 401 T Electro Magnetic Theory C 401 T Computer Oriented Numerical Methods
Friday 7 th September 2012	E 402 T Computer Technology C 402 T Accounting & Financial Management
Tuesday 11 th September 2012	E 403 T Power Electronics C 403 T Data Base Management systems
Wednesday 12 th September 2012	E 404 T Programming Language (C) C 404 T System Analysis and Design
Thursday 13 th September 2012	C 405 T Operating Systems
SECOND SEMESTER	
Tuesday 18 th September 2012	EC 201 T English –II
Wednesday 19 th September 2012	EC 202 T Mathematics –II
Thursday 20 th September 2012	E 203 T Electronic Circuits C 203 T Data Structures
Monday 24 th September 2012	E 204 T Digital Integrated Circuits C 204 T Computer Organisation
Tuesday 25 th September 2012	E 205 T Electronic Instrumentation C 205 T Programming Language –II

Calicut University P.O
Dated: 10.08.2012

Sd/-
CONTROLLER OF EXAMINATIONS