

UNIVERSITY OF CALICUT  
(Pareeksha Bhavan)

**TIME TABLE FOR THE SECOND SEMESTER M.TECH. INTERNAL  
SPECIAL SUPPLEMENTARY EXAMINATION-MARCH -2013**

Centre : National Institute of Electronics and IT , Calicut  
Time of Examination : 9.30 am to 12.30 pm

Day and Date	Subject
<b>EMBEDDED SYSTEMS</b>	
Wednesday 20 <sup>th</sup> March 2013	ES 10 203 Product Design and Quality Management
Friday 22 <sup>nd</sup> March 2013	ES 10 204 A Embedded Applications in Power Conversion B Modern Control Systems Design C Information Security
<b>ELECTRONICS DESIGN TECHNOLOGY</b>	
Wednesday 20 <sup>th</sup> March 2013	EDT 11 201 Design of Digital Signal Processing Systems
Friday 22 <sup>nd</sup> March 2013	EDT 11 202 Multimedia Compression Techniques
Tuesday 26 <sup>th</sup> March 2013	EDT 11 203 Product Design & Development
Tuesday 2 <sup>nd</sup> April 2013	EDT 11 204 A Embedded Applications in Power Conversion B Modern Control Systems Design C Information Security
Thursday 4 <sup>th</sup> April 2013	EDT 11 205 A High Speed Digital Design B ASEC & SOC C Embedded OS & RTOS

Calicut University.PO  
Dated: 14-03-2013

Sd/-  
CONTROLLER OF EXAMINATIONS