



**KANNUR UNIVERSITY**  
**(EXAMINATION BRANCH)**

No. EGI.2/IHRD-TT/05

Kannur University Campus P O,  
Dated : 11.12.2013

**TIMETABLE FOR III<sup>rd</sup> SEMESTER M Sc ELECTRONICS (IHRD)**  
**(REGULAR/SUPPLEMENTARY/IMPROVEMENT) DEGREE**  
**EXAMINATIONS, JANUARY 2014**

(Time : 9.30 AM to 12.30 PM)

Day & Date	Paper Code	Subject
10.01.2014 Friday	MET 301	Embedded Systems
13.01.2014 Monday	MET 302	VLSI Design Analysis
15.01.2014 Wednesday	MET 303	<b><u>Optional I</u></b> MEO-1 Control Systems
17.01.2014 Friday	MET 303	MEO-2 Data Communication Network
20.01.2014 Monday	MET 304	<b><u>Optional II</u></b> MEO-4 Industrial Electronics
		MEO-5 Medical Electronics

Sd/-  
JOINT REGISTRAR(EXAMS)