

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD KUKATPALLY, HYDERABAD - 500 085 EXAMINATION BRANCH

TIME-TABLE OF M.TECH. I-SEM. SUPPLY EXAMS- (EEE) BRANCH - (NR - 2007 & 08 BATCHES) - OCTOBER,2013

COURSES → DATE & DAY	Power Electronics & Electric Drives	Electrical Power Systems	Power Electronics	Electrical Power Engineering	Power Engineering & Energy Systems	Power Systems (High Voltage)	Control Systems
21.10.2013 Monday	Analysis of Power Electronic Converters	Reliability Engg.	Analysis of Power Electronic Converters	Analysis of Power Electronic Converters	Analysis of Power Electronic Converters(E - II)	Extra High Voltage Transmission	State & Parameter Estimation Theory
23.10.2013 Wednesday	Microprocessors & Micro Controllers	Microprocessors & Micro Controllers	(E - II) 1.Operations Research	Microprocessors & Micro Controllers	Microprocessors & Micro Controllers	(E-I) Microprocessors & Micro Controllers	Advanced Microprocessors
25.10.2013 Friday	Power Electronic Control of DC Drives	(E - I) 1.Energy, Auditing, Conservation & Management 2.Reactive Power Compensation & Management	Power Electronic Control of DC Drives	(E-I) 1.High Voltage Engg. & Insulation Co-ordination 2. Voltage Stability 3. Operations Research	(E-I) High Voltage Engg.& Insulation Co-ordination	High Voltage Engineering	Programmable Logic Controllers – Principles & their Appl.
28.10.2013 Monday	(E - I) 1. Digital Control Systems 2.Operations Research	Advanced Control Systems	Modern Control Theory	Modern Control theory		Advanced Control Systems	Advanced Control Systems
30.10.2013 Wednesday	(E - II) 1. Energy Conversion Systems 2.Neural & Fuzzy Systems	Flexible AC Transmission Systems	(E-I) 1.Program.Logic Controllers & its Appl. 2.Dynamics of Electrical Machines 3.Energy Conversion Systems	Energy Conversion Systems Extra High Voltage Transmission	Energy Conversion Systems	(E-I) Electrical Distribution Systems	Advanced Instrumentation Systems
01.11.2013 Friday	Machine Modelling & Analysis	Distribution Automation	(E - II) 2. HVDC Transmission 3.Modern Power Electronics	HVDC Transmission	HVDC Transmission	HVDC Transmission	Digital Control Systems
04.11.2013 Monday			Machine Modelling & Analysis	Power System Dynamics	Power System Dynamics	Dielectric and Insulation Engineering	

TIME-TABLE OF M.TECH. I-SEM.SUPPLY. EXAMS- (MECHANICAL) BRANCH - (NR - 2007 & 08 BATCHES) - OCTOBER,2013

COURSES → DATE & DAY	Advanced Manufacturing Systems	Industrial Engineering and Management	CAD/CAM	Design for Manufacturing	
21.10.2013 Monday	Materials Technology	Material & Management	Numerical Methods for Partial Differential Equation	Materials Technology	
23.10.2013 Wednesday	Design for Manufacturing	Financial Management	Stress Analysis & Vibration	Advanced Mechanics of Solids	
25.10.2013 Friday	Automation in Manufacturing	Productions & Operations Management	Elective – I: 1.Mechatronics 2.Design for Manufacturing 3. Manufacturing Methods & Mechanics of Composites	Production Design & Development Strategies	
28.10.2013 Monday	Advances in Manufacturing Technology	Industrial Engineering Practice	Design Optimization	Advances in Manufacturing Technology	
30.10.2013 Wednesday	Elective - I: 1. Quality Engineering & Manufacturing 2. Product Data Management 3. Optimization Techniques & Appl.	Statistical Quality Control	Finite Element Analysis	Finite Element Analysis	
01.11.2013 Friday	Theory of Metal Cutting & Tool Design	Elective - I: 1. Total Quality Mngt. 2. Optimization Techniques & Appl 3. Management Theory & Practice	Computer Aided Design	Elective - I: 1. Quality Engineering & Manufacturing 2. Product Data Management	
04.11.2013 Monday				Precision Engineering	

TIME-TABLE OF M.TECH. I-SEM. SUPPLY EXAMS- (ECE) BRANCH - (NR - 2007 & 08 BATCHES) - OCTOBER,2013

COURSES → DATE & DAY	Embedded Systems	Digital Systems & Computer Electronics	Digital Electronics & Communication Systems	VLSI System Design	Wireless & Mobile Communication	Computers & Communication Engg.
21.10.2013 Monday	(E-I) 1.VLSI Technology & Design 2.Advanced Computer Architecture 3. RF & Microwave Integrated Circuits	VLSI Technology & Design	Advanced Digital Signal Processing	VLSI Technology & Design	Advanced Digital Signal Processing	Database Management Systems
23.10.2013 Wednesday	(E - II) 1. Digital Data Communications 2.Image and Video Processing 3.Algorithm Analysis & Design	Digital Data Communications	Digital Data Communications	(E-I) 1.CPLD & FPGA Architecture & Appl 2.Digital Data Communications 3.Computational Techniques in Micro Electronics	Digital Data Communications	Internet Protocols
25.10.2013 Friday	Embedded Systems Concepts	(E-I) 1.Advanced Operating Systems 2.Network Security & Cryptography 3.Artificial Intelligence	(E-II) 1.Advanced Operating Systems 2. Advanced Computer Architecture 3. Artificial Intelligence	(E - II) 1. Embedded Systems Concepts 2.VHDL Modelling of Digital Systems 3.Modelling & Synthesis with Verilog HDL	Mobile Communications	Advanced Commn. Systems
28.10.2013 Monday	System Modelling & Simulation	Neural Networks & Applications	(E-I) 1.Embedded &Real Time Systems 2.Neural Networks & Applications 3. Network Security & Cryptography	Electronic Design Automation Tools	Spread Spectrum Communications	Wireless Communications & Networks
30.10.2013 Wednesday	Analog & Digital IC Design	(E - II) 1.Advanced Computer Architecture 2.High Performance TCP / IP Networks 3.Low Power VLSI Design	Detection & Estimation of Signals	Analog IC Design	Transform Techniques	Computer System Architecture
01.11.2013 Friday	Micro Controllers for Embedded System Design	Digital System Design	Digital System Design	Digital System Design	Low Power VLSI Design	Embedded System Design

CONTINUATION.... TIME-TABLE OF M.TECH. I-SEM. SUPPLY EXAMS- (ECE) BRANCH - (NR - 2007 & 08 BATCHES) OCTOBER,2013

COURSES → DATE & DAY	Communication Systems	Web Technologies	Embedded Systems & VLSI Design	Image Processing	VLSI & Embedded Systems	Electronics & Communication Engg.	Aerospace Engineering
21.10.2013 Monday	VLSI Technology & Design	Internet Operating Systems	Low Power VLSI Design	Digital Image Processing	(E-I) 1.Low Power VLSI Design 2. Hardware Software Co-design	Statistical Signal Processing	Engineering Analysis of Flight Vehicles
23.10.2013 Wednesday	Detection & Estimation Theory	Internet Protocols	Micro Controllers for Embedded System Design	Pattern Recognition	Micro Controllers for Embedded System Design	Digital Data Communication	Continuum Mechanics
25.10.2013 Friday	Satellite Communications	Software Engineering for Web Appl	Embedded System Concepts	Real Time Operating System	Embedded System Concepts	VLSI Architecture & Design Methodologies	Air Transportation Systems
28.10.2013 Monday	Coding Techniques	Data on the Web	VHDL Modelling of Digital Systems	Neural Network	VHDL Modelling of Digital Systems	(E-I) 1.VHDL Modelling of Digital Systems	(E - I) 1.Aerodynamics of Flight Vehicles 2. Flight Vehicle Structures
30.10.2013 Wednesday	Digital Communications Theory & System	Multimedia &Application Development	Analog & Digital IC Design	Advanced Computer Graphic	Analog & Digital IC Design	Coding Theory & Practice	(E - II) 1.Modeling & Simulation of Fluid Flows 2. Computational Structural Analysis
01.11.2013 Friday	Advanced Radiating Systems	Fundamentals of Web Technologies	Embedded Software Design	Advanced Data Structures	(E - II) 1.Embedded Software Design 2.RF& Microwave Integrated Circuits	(E-I) 2.Spread Spectrum Techniques 3.RF & Microwave Integrated Circuits	Mathematical Modeling
04.11.2013 Monday	Linear Algebra					Advanced Digital Signal Processing	

TIME-TABLE OF M.TECH. I-SEM. SUPPLY EXAMS- (CSE) BRANCH - (NR - 2007 & 08 BATCHES) - OCTOBER ,2013

COURSES → DATE & DAY	Computer Science & Engineering	Computer Science	Software Engineering	Information Technology	Parallel Computing	Computer Networks & Information Security
21.10.2013 Monday	Design and Analysis of Algorithms	Design and Analysis of Algorithms	Unix Networking Programming	Date Warehousing & Mining	Parallel Algorithms	Distributed Computing
23.10.2013 Wednesday	Computer Organization	Computer Organization	Software Requirements and Estimation	Middleware Technologies	Parallel Computer Architecture	Data Warehousing & Mining
25.10.2013 Friday	Computer Communications	Computer Communications	Software Process and Project Management	Network Programming	Distributed Operating Systems	Network Programming
28.10.2013 Monday	Operating Systems	Operating Systems	Web Technologies	Web Technologies	Web Technologies	Web Technologies
30.10.2013 Wednesday	Software Engineering	Mathematical Foundations of Computer Science	Wireless Networks and Mobile Computing	Software Project Management	Adhoc & Sensor Networks	Advanced Computer Networks
01.11.2013 Friday	Database Management Systems	Database Management Systems	Object Oriented Modelling	Network security & Cryptography	Embedded Systems	Information Security - I
04.11.2013 Monday				Advanced Computer Architecture		Soft Skills – I

<u>TIME-TABLE OF M.TECH. I-SEM. SUPPLY EXAMS- (NR - 2007 & 08 BATCHES) - OCTOBER,2013</u> - (BIOTECHNOLOGY & CHEMICAL ENGINEERING) COURSES

TIME: 02.00 P.M TO 05.00 P.M

<u>COURSES</u> → DATE & DAY	BIOTECHNOLOGY	CHEMICAL ENGINEERING
21.10.2013 Monday	Molecular Biology & Virology	Applied Numerical Methods
23.10.2013 Wednesday	Microbial Engineering	Advanced Chemical Engineering Thermodynamics
25.10.2013 Friday	Preparatory Core Course - II: 1. General & Industrial Microbiology 2. Process Engineering Principles	Advanced Transport Phenomena
28.10.2013 Monday	Preparatory Core Course - I: 1. Biochemistry & Metabolic Regulation 2. Basic Engineering Mathematics	Petroleum Refinery Engineering Advanced Environmental Engineering
30.10.2013 Wednesday	Immunology	Industrial Microbial Products Advanced Bio-chemical Engineering
01.11.2013 Friday	Enzyme Engineering	Advanced Chemical Reaction Engineering

NOTE: 1) ANY CLASHES/OMMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.
2) IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES THE EXAMS WILL BE CONDUCTED AS USUAL.

Sd/-XXXXX

Date: 23.09.2013 CONTROLLER OF EXAMINATIONS

To,

The Principals of JNTUH Affiliated Colleges Offering M.Tech. Courses. Copy to D.E Peshi, C.E, A.C.E. AND COMPUTER SECTION.