



Jawaharlal Nehru Technological University Anantapur
Examinations Branch
Rectified Timetable

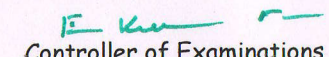
M.Tech - II Semester Regular Examinations December 2013
(for students admitted in 2012 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Micro & Nano Electronics (MNE)	VLSI & Embedded Systems (VLSIES), Embedded Systems & VLSI (ESVLSI), VLSI & Embedded Systems Design (VLSIESD)	
04.12.2013 Wednesday	MEMS/NEMS Design 12D86201	Elective-II	Software Defined Radio 12D68204
			VLSI Signal Processing 12D68205
			Design & Testability 12D68206
06.12.2013 Friday	Carbon Nano Tubes & Applications 12D86202		Real Time Operating Systems 9D55202 (Common Paper)
09.12.2013 Monday	Nano Biotechnology 12D86203		DSP Architectures & Applications 12D68202
11.12.2013 Wednesday	Nano Sensors 12D86204		FPGA Architectures & Applications 9D55204
13.12.2013 Friday	Nano Fabrication Techniques 12D86205		ASIC Design 9D68203
16.12.2013 Monday	Elective-II	Nano Medicine 12D86206	Memory Technology 12D68201
		Packing Technology 12D86207	
		Industrial Nanotechnology 12D86208	

- NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.
2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

First Notified on: 06.11.2013
Rectified on: 16.11.2013


Controller of Examinations