



Jawaharlal Nehru Technological University Anantapur
Examinations Branch
Rectified Timetable

M.Tech - I Semester Supplementary Examinations December 2013

(for students admitted in 2009, 2010, 2011 & 2012 only)

Time: 10.00 AM to 01.00 PM

Date / Day	Digital Systems and Computer Electronics (DSCE)	Digital Electronics & Communication Systems (DECS)	Electronics & Communication Engineering (ECE)
05.12.2013 Thursday	ELECTIVE - I Network Security & Cryptography 9D06106a	Advanced Digital Signal Processing 9D38103 (Common to DECS & ECE)	
07.12.2013 Saturday	Embedded System Concepts 9D06102 (Common to DSCE, DECS, ECE & ES)		ELECTIVE - I Embedded System Concepts 9D06102 (Common to DSCE, DECS, ECE & ES)
10.12.2013 Tuesday	Advanced Computer Architecture 9D06103 (Common to DSCE, DECS & ES)	ELECTIVE - I Advanced Computer Architecture 9D06103 (Common to DSCE, DECS & ES)	Detection & Estimation of Signals 9D38205 (Common to Com Sys & ECE)
12.12.2013 Thursday	Advanced Data Communications 9D06104	Digital Communication Techniques 9D38104 (Common to DECS & ECE)	
14.12.2013 Saturday	Neural Networks & Applications 9D06105 (Common to DSCE & ECE)	Adaptive Signal Processing 9D38105	ELECTIVE - I Neural Networks & Applications 9D06105 (Common to DSCE & ECE)
17.12.2013 Tuesday	ELECTIVE - I	DSP Processors & Architectures 9D06106b (Common to DSCE, DECS & ECE)	ELECTIVE - I DSP Processors & Architectures 9D06106b (Common to DSCE, DECS & ECE)
		Lower Power VLSI Design 9D06106c (Common to DSCE & DECS)	
19.12.2013 Thursday	Digital System Design 9D06101 (Common to DSCE, DECS, ECE, VLSIES, ESVLSI, VLSIESD & MNE)		
21.12.2013 Saturday	-	-	Optical Communications 9D38106b

- NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.
 2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

First Notified on: 06.11.2013

Rectified on: 20.11.2013

E. Kumar
 Controller of Examinations
J. N. S.



Jawaharlal Nehru Technological University Anantapur

Examinations Branch

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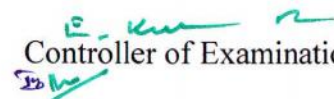
Date / Day	VLSI & Embedded Systems (VLSIES), Embedded Systems & VLSI (ESVLSI), VLSI & Embedded Systems Design (VLSIESD)	Micro & Nano Electronics (MNE)	Control Systems (Con.Sys)
05.12.2013 Thursday	VLSI Technology 9D57101 (Common to VLSIS, VLSISD, VLSI, VLSID, ES, VLSIES, ESVLSI & VLSIESD)	ELECTIVE-I Quantum Electronics 12D86106a Measurements & Characterization of Nano Materials 12D86106b	Intelligent Control 12D75103
07.12.2013 Saturday	Embedded System Concepts 9D06102 (Common to DSCE, DECS, ES, VLSIES, ESVLSI & VLSIESD)	Microelectronic Technology & IC Fabrication 12D86102	Stochastic Processes 12D75101
10.12.2013 Tuesday	Analog & Digital IC Design 12D68101	Materials for Nanotechnology 12D86103	ELECTIVE-II Principles of Machine Modeling & Analysis 9D54103 (Common to PE&ED, PE & Con.Sys) Advanced Microprocessors & Microcontrollers 12D75105
12.12.2013 Thursday	Hardware Description Languages 9D57104 (Common to VLSIS, VLSISD, VLSI, VLSID, VLSIES, ESVLSI & VLSIESD)	Micro Sensors & Actuators 12D86104	Digital Control Systems 12D75102
14.12.2013 Saturday	ELECTIVE-I MEMS 12D68102a RF IC Design 12D68102b Device Modeling 12D68102c	Nano CMOS Circuits & Physical Design 12D86105	Modern Control Theory 12D75100
17.12.2013 Tuesday	-	Advanced Computing Methods 12D86101	ELECTIVE-I Robot Modeling & Control 12D75104a Advanced Instrumentation Systems 12D75104b
19.12.2013 Thursday	Digital System Design 9D06101 (Common to DSCE, DECS, ECE, VLSIES, ESVLSI, VLSIESD & MNE)	ELECTIVE-I Digital System Design 9D06101 (Common to DSCE, DECS, ECE, VLSIES, ESVLSI, VLSIESD & MNE)	-

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