

VFSTR UNIVERSITY:: VADLAMUDI

M.Tech I Year II Sem - Sup - R13 - July - 2015 Results

Sl. No	Regd. No	Subject Code	Subject Name	Internal Marks	External Marks	Total	Pass / Fail
1	131FB06016	EC502	RTOS for Embedded Systems	32	42	74	P
2	131FB06019	EC502	RTOS for Embedded Systems	30	32	62	P
3	131FB06016	EC526	FPGA & CPLD Architecture & Applications	35	31	66	P
4	131FB06019	EC526	FPGA & CPLD Architecture & Applications	28	28	56	P
5	131FB13007	EE520	Power System Dynamics & Stability	37	33	70	P
6	131FB04020	CS504	Network Security & Cryptography	37	13	50	F
7	131FB04011	CS508	Embedded Systems	31	43	74	P
8	131FB06019	EC514	RTOS for Embedded Systems Lab	31	50	81	P

K. Hey

Asst. Controller of Examinations

[Signature]
Controller of Examinations

VU/CE/CIR/15/723 (BG)

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: website.