

University of Hyderabad
(A Central University established by an Act of Parliament)
Prof. C.R. Rao Road, P.O. Central University, Hyderabad 500 046

No.UH/Acad/EE/2015 /

O/o. the Controller of Examinations
Academic Section
Dated : 03-01-2016

Notification of Results of Entrance Examination of Visvesvaraya Ph.D. Scheme
(Interview for admission to **Ph.D. Electronics Science**).

Based on the performance in the written test held on 03-01-2016, the following candidates are called for interview for admission to **Ph.D. Electronics Science**.

Venue: Committee Room, School of Physics, University of Hyderabad Campus, Hyderabad - 500 046
Date of Interview : 04-Jan-2016 Time: 09.00 a.m.

S.No.		HTNO	NAME
1	#	14079128	N KOTESWARA RAO
2	#	14079135	RAMASRI THALLA
3	#	14079136	GOODELLI SRIKANTH
4	#	14079150	S. VENU MADHAV
5	#	14079152	SAROJ KUMAR MOHARANA
6	#	14079153	SATHISHKUMAR KUSUMA
7		14079121	SEEMAKURTHI SAI KRISHNA
8		14079140	VENKATA REDDY KOPPARTHI

= OBC

OBC candidates shall be required to submit latest OBC certificate (obtained on or after 1.4.2014) in the Govt. of India prescribed format in support of their claim issued by a competent authority, without which their claim shall not be considered for admission under OBC category. Candidates under OBC category are required to note that OBC certificate format prescribed by the Govt. of India is different from the BC certificate issued by the respective State Government/s. Therefore, candidates belonging to Non-creamy Layer OBC category should submit only the OBC certificate but not the BC certificate.

Candidates are informed to bring their original certificates and the Hall ticket for verification at the time of interview.
The candidate will attend the interview at their own cost.

Controller of Examinations