



**NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI-620 015.  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Date: 3-12-2012**

**WALK-IN INTERVIEW**

**Recruitment of Temporary Post under the SMDP-II project  
[Sponsoring Authority: DeitY, Govt. of India]**

Interested candidates are requested to appear for a walk-in interview on **10<sup>th</sup> December 2012 at 3.00 P.M.** for the following temporary posts under SMDP-II Project, Department of Electronics and Communication Engineering, National Institute of Technology, Tiruchirappalli -620015.

**Technical Officer:** 2 posts, **Lab Engineer:** 1 post.

**Qualification & Experience:**

I Class M.E./M.Tech in VLSI system/ Microelectronics/ Communication System with at least 1 year experience on VLSI CAD tools. The B.E./ B.Tech. degree may be in any one of the circuit branches (CSE/ECE/EEE/ICE/IT) from a recognized University.

**Desirable Experience:**

Teaching and industrial experience in the field of VLSI is preferable.

**Salary:**

Rs.20,000/- to Rs.24,000/- (consolidated) depending upon the qualification and experience.

The post is purely temporary in nature.

**Registration for Ph.D.:**

Temporary staff, if found suitable, may register for part-time Ph.D.

**No TA/DA will be paid.**

**The selected candidates will be required to join immediately**

Interested candidate may appear for the interview with all relevant certificates in original on Monday 10<sup>th</sup> December, 2012 at 3 P.M.

An advance soft or hard copy of the application form in the prescribed format and CV in plain paper may be mailed to

Dr. G.Lakshminarayanan,  
Co-coordinator/ SMDP II,  
Department of Electronics and Communication Engineering,  
National Institute of Technology,  
Tiruchirappalli -620015  
Mail id: laksh@nitt.edu

**Venue of the interview:**

Conference Room,  
Department of Electronics and Communication Engineering,  
National Institute of Technology,  
Tiruchirappalli -620015

**Information to the candidates****Duties and responsibilities for Technical officers & Lab engineers (No. of posts: Two)**

- 1) Handling theory as well as laboratory courses on VLSI systems for UG and PG students
- 2) Evaluation of the students and awarding grades on the theory and laboratory subjects handled
- 3) Guiding the UG/PG students on their project work in VLSI design & testing
- 4) Undergoing training on the advanced VLSI design topics under the Learning Enhancement programme (LEP) to be conducted at the VLSI man power resource centers.
- 5) Preparation and submission of project reports to the project funding and monitoring agencies
- 6) Developing the skills on advanced VLSI CAD tools and guide the students on chip design under India chip program.
- 7) Any other duties that may be assigned by the project coordinator and co-coordinator
- 8) Installing and Maintaining CAD tools in Windows and Linux Environment

**Duties and responsibilities for Lab engineer (No. of posts : one )**

- 1) Installing and Maintaining CAD tools in Windows and Linux Environment
- 2) Handling laboratory courses on VLSI systems for UG and PG students
- 3) Evaluation of the students and awarding grades on laboratory subject handled
- 4) Guiding the UG/PG students on their project work in VLSI design & testing
- 5) Undergoing training on the advanced VLSI design topics under the Learning Enhancement programme (LEP) to be conducted at the VLSI man power resource centers
- 6) Preparation and submission of project reports to the project funding and monitoring agencies
- 7) Developing the skills on advanced VLSI CAD tools and guide the students on chip design under India chip program.
- 8) Any other duties that may be assigned by the project coordinator and co-coordinator



# NATIONAL INSTITUTE OF TECHNOLOGY TIRUCHIRAPPALLI- 620 015

APPLICATION FOR THE TEMPORARY POST OF TECHNICAL OFFICER/  
LAB. ENGINEER

[Please tick the relevant post(s)]

Recent  
Passport size  
photograph to  
be affixed.

1.	Name in Full (Capital Letters) (as in SSLC Certificate)					
2.	Date of Birth	Day	Month	Year		
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3	Address to which Communications should be sent  (Also furnish email, fax, telephone number, if any)					
4.	Permanent Home Address					
5.	Name and Address of Father / Husband					
6. Details of University Education from SSLC onwards						
Degree	Discipline	University	Class/Divis ion	% Marks	Year of Passing	

7 Professional Experience in chronological order upto the present post				
Organisation	Designation	From	To	Scale of Pay

8	List of papers published/ awards & distinctions won

9	List of programming languages and VLSI CAD tools in which you have experience

10	List training programmes attended

11	List of subjects taught (if any)

