

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085**

**JNTUH and SEER AKADEMI
M.S.(VLSI & EMBEDDED SYSTEMS DESIGN)**

FINAL PHASE COUNSELLING SCHEDULE

Dt: 13.08.2012

Few seats are left over after Ist phase counselling of M.S.(VLSI & Embedded Systems Design) – 2012 in JNTUH College of Engineering, Srinidhi Institute of Science & Technology and CMR College of Engineering centres. The candidates who already applied and fresh eligible candidates with valid GATE Rank, PGECET - 2012 rank and qualified in the Entrance Examination conducted by JNTUH University in the month of July-2012 are required to attend the counseling / sliding on **22.08.2012 at 10.00 am** at Admissions Section, Counselling Hall, JNTUH, Kukatpally, Hyderabad –85 with following Original Certificates and Fee.

ORIGINAL CERTIFICATES TO BE SUBMITTED:

- (1) Original Rank Card / Hall Ticket (valid GATE / PGECET rank card-2012).
- (2) Date of Birth Certificate (SSC/SSLC/HSC/MP/CBSC)
- (3) Convocation Original Degree Certificate / Provisional Certificate of the qualifying Examination (B.Tech. / B.E.)
- (4) Marks Memos of Qualifying Examination (B.Tech. / B.E.).
- (5) Transfer Certificate / Migration Certificate.
- (6) Copies of 1,2,3 and last pages of Passport (optional).

FEE :

The candidates should pay following fee at the time of counselling, if selected for admission.

Tuition fee for Rs. 60,000/- (for 1st semester) and Counselling fee for Rs. 1,000/- (if not paid earlier).

The fresh eligible candidates with valid GATE & PGECET Rank holders should pay Registration Fee an amount of Rs. 1000/- extra.

DIRECTOR, ADMISSIONS