

Grams: "TECHNOLOGY"  
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**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA**

KAKINADA-533 003. ANDHRA PRADESH (India)

(Established by Andhra Pradesh Act No.30 of 2008)

Lr.No.JNTUK/ DA/M.Tech (Self finance) /VLSI Entrance Test/Merit List

Date : 08-09-2014

**Dr. G.V.R. Prasada Raju,**  
**B.E, M.E., Ph.D**

**REGISTRAR**

**CIRCULAR**

The provisionally merit list of the candidates who appeared the M.Tech. (VLSI Design) entrance test conducted on 07-09-2014 at JNT University Kakinada is herewith notified. Selected candidates are required to attend the counseling in the SENATE HALL, Administrative Building as per following schedule.

**COUNSELING SCHEDULE**

<b>Date</b>	<b>Time</b>	<b>Reporting Time</b>	<b>Fee details</b>
<b>12-09-2014 (Friday)</b>	<b>2:00 PM</b>	<b>M.Tech. (VLSI Design)</b>	<b>Rs. 35,000/- per Semester</b>

**NOTE: Mere attending for counseling will not guarantee admission, since more than required candidates are called for counseling.**

The Candidates are requested to submit the following **Original Certificates** at the time of counseling:

- SSC.
- Intermediate Certificate
- Degree Certificate.
- Marks Memos of Qualifying Degree
- Transfer and Conduct Certificate.
- One set of attested copies of all the certificates.
- Three self addressed envelopes of 25 X 10 cms size duly stamped (affixing Rs 6 stamps).
- 4 Passport size photos.
- Demand Draft for tuition fee drawn in favor of Registrar, JNTUK, Kakinada, payable at Kakinada.
- Demand Draft for counseling fee for **Rs.500/-** drawn in favor of Director Admissions, JNTUK, , payable at Kakinada

**Sd/- REGISTRAR**

Encl: Merit List of the Candidates

Cc : Secretary to Hon'ble Vice Chancellor, JNTU KAKINADA  
Director of Admissions, JNTU KAKINADA