



Indian Institute of Technology Hyderabad  
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भारतीय प्रौद्योगिकी संस्थान हैदराबाद  
आयुध निर्माणी एस्टेट  
येदुमैलारम ५०२२०५, आंध्र प्रदेश, भारत  
फोन : (०४०) २३०१ ६०३३; फेक्स : (०४०) ६००३, ६०३२

Date: 16/05/13

Walk in Interview are invited for the posts of Project Assistant (on contract)/ Project Associate (on contract)/ with a **strong VLSI Chip (Analog, Digital) Layout experience** in the project entitled "**IoT for Smarter Healthcare**". The project is sponsored by DIT

**Name of the Position:** Project Associate (on contract)/ Project Assistant (on contract)

**Minimum Qualification:** MTech in ECE/EE specialization on Microelectronics & VLSI/ RF (Submitted/awarded) for Project Associate/ Diploma/ BTech in EE/ECE with work experience in VLSI CHIP Layout experience for Project Assistant

**Emoluments:** 10,000-18,000/- per month

**Duration of the Project:** ONE Year (Subsequent Extension based on Performance)

**Essential requirement:** Strong knowledge of CMOS, IC design , VLSI Chip (Analog, Digital)Layout experience

Interview will be preceded by a screening test

Selected candidates will work on development of chip for "**IoT for Smarter Healthcare**".

Selected candidates may be permitted to PhD program as per existing institute rules.

Interested candidates send their CV by email (vlsiadmin@iith.ac.in) before **June 1st, 2013**. Please write " EE-IoT/ IoT for Smarter Healthcare" in the subject line.

Walk in Interview Date: **7<sup>th</sup> June, 2013**

Venue: **IIT Hyderabad , <http://www.iith.ac.in/directions.html>**

Time : 9 a.m.

Shri SCS  
2  
16/5/13

P. Rajalaxmi  
16/5/13

Amitha Choudhary  
16.05.13