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## Ph. D Entrance Test

Date: **11.01.2014 (Wednesday)**

Paper I (Objective Type): **50 Marks, 11 a.m-12 noon.**

Syllabus: Quantum Mechanics, Solid state Physics, Nanoscience, Circuit theory, Digital Electronics, Communication System, Embedded Systems (PG level)

Paper II (Seminar presentation): **50 marks, from 1 p.m.**

Time allotted for each candidate for

Seminar: 20 minutes for presentation.

10 minutes for Q-A session

Topic of Seminar: Any topic related to proposed Ph.D work of the candidate.

Mode of Presentation: **Power Point.**

T. Datta  
24/12/13