## CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

## Advt. No. 01/2013

## "Walk -in- Interview"

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified incumbents for purely temporary positions of "Project Fellow/Senior Project Fellow" for various ongoing Sponsored/Network Projects in the Areas of Semiconductor Devices, Electronic Systems and Microwave Tubes at CSIR-CEERI.

Category	No. of	Consolidated	Qualifications	Age	
Semiconduc	positions tor Daviges	emoluments			
Project	24	₹16000/-	1st Class B.E./B.Tech. (Electronics/ Electrical &		
Fellow		per month (fixed)	Electronics/Electronics & Commun./Bio-electronics/ Electronics & Telecommunication/ Electronics & Instrumentation / Chemical ) OR 1 <sup>st</sup> Class M.Sc. (Physics/Electronics/ Electronics & Commun./ Materials Science/ Semiconductor technologies/Chemical/ Bio-electronics) OR equivalent.	32 yrs.	
Sr. Project Fellow	34	₹18000/- per month (fixed)	M.E./M.Tech./M.S. (Nano-technology /Microelectronic /VLSI Design & Technology/ Technology /Bio-electronics/ Electronics & Telecommunication/ Electrical & Electronics/Electronics & Instrumentation/ Optoelectronics / Photonics/Lasers/ Materials Science/ Electronics & Commun./ /Microwave) OR equivalent.  OR  1st Class B.E./B.Tech. (Electronics/ Electrical & Electronics/Electronics & Commun./ Bio-electronics/ Electronics & Telecommunication/ Electronics & Instrumentation /Chemical) OR 1st Class M.Sc. (Physics/Electronics/ Electronics & Commun./ Material Science /Semiconductor technologies/Chemical/ Bio-electronics) OR equivalent with two years research experience.		
Electronic S	ystems Area	•	1		
Project	11	₹16000/-	1 <sup>st</sup> Class B.E./B.Tech.(Electronics/ Electrical &	28 yrs.	
Fellow		per month (fixed)	Electronics/Electronics& Commun. /Instrumentation/Instrumentation & Control /Mechatronics/Computer Science/IT/Bio-technology) OR 1 <sup>st</sup> Class M.Sc. (Physics/Electronics/Electronics & Commun./Tech./ CSE/IT/ Bio-technology) OR equivalent.	J	
Sr. Project Fellow	06	₹18000/- per month (fixed)	M.E./M.Tech./M.S. (Electronics/VLSI/ECE/ Computer Science/ Embedded System/Mechatronics/ Instrumentation & Control/ Electrical/Robotics/ Communication Systems/Space Communication/ Satellite Communication/Advanced IT/Intelligent Systems) OR equivalent.  OR  1st Class B.E./B.Tech.(Electronics/ Electrical & Electronics/Electronics & Commun./Instrumentation/ Instrumentation & Control /Mechatronics/Computer	32 yrs.	

			Science/IT/Bio-technology) OR 1 <sup>st</sup> Class M.Sc. (Physics/Electronics/Electronics & Commun./Tech./ CSE/IT/ Bio-technology/Technology) OR equivalent with two years research experience.			
Microwave Tubes Area						
Project	01	₹16000/-	1 <sup>st</sup> Class B.E./B.Tech. (Electronics) OR 1 <sup>st</sup> Class M.Sc.	28 yrs.		
Fellow		per month	(Physics/Electronics) OR equivalent.			
		(fixed)				

Age relaxation for SC/ST/OBC candidates will apply as per GOI instructions. The number of positions are tentative and may vary.

In case suitable candidates for the positions of Senior Project Fellow with essential qualifications as given above are not available, these positions would be filled up as Project Fellow with qualifications of Project Fellow as given above on fixed remuneration of ₹16,000/- per month (fixed).

Tenure of engagement: Six months in the first instance, likely to be extended in steps of six

months for the duration of Project based on satisfactory performance. The total tenure as Project Fellow/Senior Project Fellow shall not exceed 5 years in any case. The total tenure of five years shall be calculated as per periods spent on one Project and/or different Projects taken together in CSIR-CEERI, Pilani or any other Lab./Instt. of CSIR as Project Fellow/Senior Project Fellow (formerly known as Project Assistant Level II and Project Assistant Level III) or as any other

designation of equal status.

Project Fellows/Senior Project Fellow will be entitled for one day leave Leave:

for each completed month's service.

Enhancement in On completion of 2 years, the performance of the individual shall be Remuneration:

adjudged by an internal committee. On the recommendations of the

Committee enhancement ₹2000/- per month shall be considered at each

level.

The engagement of Project Fellows/Senior Project Fellow will be made on behalf of the Sponsorer of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to any claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.

Candidates who fulfil the eligibility criteria on the date of interview may appear for "Walk-in-Interview" in the Conference Room of this Institute on 02.02.2013 from 09:00 am to 01:00 pm with his/her Bio-data in the prescribed proforma available at CSIR-CEERI website alongwith original documents of qualifications semester-wise/year-wise and caste certificate, if any (not older than 06 months in case of OBC) with one set of attested copies thereof for each area/level.

No candidate shall be allowed for registration after 01:00 pm.

No TA/DA for appearing for interview will be paid to the candidates.

ADMINISTRATIVE OFFICER

## PROFORMA OF APPLICATION FOR THE POSITOINS OF PROJECT FELLOW/Sr. PROJECT FELLOW

Area:	Positio	on for which appl	lied:			
1. Name of the C	Candidate :					
2. Father's Name	e :			Photo		
3. Date of Birth	:	:				
4. Address for C with contact N		:				
5. Permanent Ad	ldress :					
6. Qualifications	s (Secondary onwards):					
Examination Passed	Name of Board/University	Year of passing	Subject	Division/ %age		
		rg		7.5.85		
7. Research Exp	erience					
8. Whether SC/S (in case of OB	ST/OBC/PH C, caste certificate should not be o		onths)			
	vious employment/n any CSIR Lab./Instt.					
engagement i	ii any CSIK Lao./fiistt.					
10. In case, apply	ving for more than one area					
please mention	on the preference of area					
Dated:						
			S	ignature of the candidate		